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## ELECTRONIC TRACKING AND RANGING SYSTEM

### TECHNICAL FIELD

[0001] This invention relates to an electronic tracking and ranging system. More specifically, an electronic tracking and ranging system that utilizes phase differences between a monitor signal and a signal received from a tracked unit, where multiple frequency components of both monitor unit and tracked unit signals are phase detected to determine the ranging distance of an object including a tracked unit from a monitor unit.

### BACKGROUND ART

[0002] There is a need for an improved electronic tracking and ranging system to account for RF signal variations due to RF signal attenuation from energy waves, such as electromagnetic energy, are being reflected off and being dissipated in surrounding areas. Present electronic tracking and ranging systems require a special calibration procedure or special settings so that a user can determine range or track an object within a given area or a given location. In addition, other problems with present electronic tracking and ranging systems for objects include the use of very fine timing intervals for accurate measurements, i.e., within a couple of nano-seconds, and inherent delays in a transponder response time and transponder variations with temperature changes may cause larger ranging variations than the time interval being measured. Thus, there is a need for an improved electronic tracking and ranging system that provides a improved solution to the above problems such as increasing accuracy of locating an object including a tracked unit, even when the transmitted signal is attenuated as well as provide other advantages over present tracking and ranging systems.

## DISCLOSURE OF THE INVENTION

**[0003]** Disclosed is an electronic ranging and tracking systems for objects that applies interferometer principles to determine distance between a monitor unit to a tracked unit. In particular, the monitor unit transmits a monitor direct sequence spread spectrum (MDSSS) signal to a tracked unit. Afterwards, tracked unit receives MDSSS signal and transmits back to monitor unit a tracked direct sequence spread spectrum (TDSSS) signal . Finally, the monitor unit receives TDSSS signal, performs a comparison to MDSSS signal, locks signals between MDSSS and TDSSS, and outputs at least one phase difference between MDSSS signal and TDSSS signal. During the locking process, TDSSS signal comprising three frequencies, i.e., a carrier frequency, a chipping frequency of pseudo-random noise (PN) sequence, and a repetition frequency of pseudo-random noise (PN) sequence, which are utilized within the monitor unit to calculate ranging distance between the tracked unit and the monitor unit.

## BRIEF DESCRIPTION OF THE DRAWINGS

**[0004]** Figure 1 is the block diagram for an interferometer electronic tracking system of the present invention.

**[0005]** Figure 2 is a schematic of a monitor unit for the present invention.

**[0006]** Figure 3 is a schematic of a tracked unit for the present invention

**[0007]** Figure 4 illustrates a first phase comparison of a monitor unit of the present invention.

**[0008]** Figure 5 illustrates a second phase comparison of a monitor unit of the present invention.

**[0009]** Figure 6 illustrates a third phase comparison of a monitor unit of the present invention.

**[0010]** Figure 7 illustrates a front view of a housing for a tracked unit for the present invention

**[0011]** Figure 8 illustrates a front view and a back view of a housing for a monitor unit for the present invention.

**[0012]** Figure 9 displays operating field of activity of the system.

## MODES FOR CARRYING OUT THE INVENTION

**[0013]** Figure 1 is the block diagram for an interferometer electronic tracking system 8. Interferometer electronic tracking system 8 comprises a monitor unit 10 and a tracked unit 12.

Monitor unit 10 comprises a monitor programmable logic array 14, a monitor reference oscillator 16, a monitor micro-controller 18, a first monitor modulator 20, a second monitor modulator 21, a monitor phase detector 22, a monitor power amplifier 24, a monitor duplexer 26, a monitor low noise amplifier (LNA) 28, an antenna switch 30, a first monitor antenna 32, and a second monitor antenna 34. Tracked unit 12 comprises a tracked programmable logic array 36, a tracked reference oscillator 38, a tracked micro-controller 39, a tracked modulator 40, a tracked phase detector 42, a tracked power amplifier 44, a tracked duplexer 46, a tracked low noise amplifier (LNA) 48, and a tracked antenna 50. Monitor unit 10 transmits a monitor direct sequence spread spectrum (MDSSS) signal 52 to tracked unit 12 including a monitor reference frequency 70 (see Figure 6) and a monitor pseudo-random noise (PN) sequence 65 (see Figure 5). MDSSS signal 52 waveform is a waveform like that used by a Wireless Local Area Network (WLAN) utilizing direct sequence spread spectrum (DSSS) technology. Tracked unit 12 receives MDSSS signal 52 and transmits to monitor unit 10 a tracked direct sequence spread spectrum (TDSSS) signal 56 including a tracked reference frequency 72 (See Figure 6) and a tracked pseudo-random noise sequence 61 (See Figure 5). TDSSS 56 waveform is a waveform like that used by a Wireless Local Area Network (WLAN) utilizing direct sequence spread spectrum (DSSS) technology. Following, monitor unit 10 receives TDSSS signal 56 and calculates ranging distance between monitor unit 10 and tracked unit 12 using multiple frequency phase comparisons between MDSSS signal 52 and TDSSS signal 56.

[0014] Figure 2 is a schematic of a monitor unit 10 for an interferometer electronic tracking system. Monitor unit 10 generates a monitor reference frequency 70 from a monitor reference oscillator 16 such as a voltage-controlled oscillator or the like. Monitor programmable logic array 14 has a phased-lock loop which locks monitor reference oscillator 16 to a multiple of a clock frequency of the monitor programmable logic array 14. The clock frequency is derived from a crystal oscillator 11. In this embodiment, monitor reference frequency 70 is a carrier frequency such as a Radio Frequency (RF). Furthermore in this embodiment, carrier frequency is 915 MHz. Monitor reference oscillator 16 output electrically connects to an input port of a first monitor modulator 20. First monitor modulator 20 functions to modulate carrier frequency with a monitor pseudo-random noise (PN) sequence 65 to generate a monitor direct sequence spread spectrum (MDSSS) signal 52 that will be sent through monitor power amplifier 24.

**[0015]** In this embodiment, monitor PN sequence 65 is applied at the rate termed the chipping frequency. Monitor PN sequence 65 modulates carrier frequency to create signal spreading of carrier frequency. In particular, chipping frequency is the rate at which monitor PN sequence spreads the bandwidth of carrier frequency. Also, chipping frequency determines desired monitor frequency bandwidth, where monitor frequency bandwidth is twice the chipping rate. In this embodiment, a 915 MHz carrier frequency with a chipping frequency of 12 MHz generates a monitor frequency bandwidth extending over a frequency range of 903 MHz to 927 MHz. Furthermore, monitor PN sequence 65 has a finite length, which finite length is divided into chipping frequency to yield a repetition rate 57 wherein monitor PN sequence 65 repeats. The length of monitor PN sequence 65 determines this repetition rate 57 because repetition rate 57 is the chipping rate divided by length of monitor PN sequence 65. In this embodiment, a monitor PN sequence 65 with a length of 40 characters results in a repetition rate 57 of 250 kHz. Alternatively, a monitor PN sequence 65 with a length of 133 characters results in a repetition rate of 75.2 kHz.

**[0016]** Within monitor programmable logic array 14, monitor PN sequence is created by a spreading sequence originating from a set of “source” registers (not shown in Figure) within monitor programmable logic array 14. Monitor PN sequence 65 loads into a first shift register 13 and a second shift register 15 within monitor programmable logic array 14, whereby monitor PN sequence is shifted at the chipping rate. In particular, monitor programmable logic array 14 including first shift register 13 with a digital tap line (not shown in Figure) controlled by monitor micro-controller 18 to spread monitor PN sequence at chipping frequency, outputting a single bit value at the chipping rate of shifted monitor PN sequence to first monitor modulator 20. Additionally, monitor programmable logic array 14 includes second shift register 15 with a digital tap line (not shown in Figure) controlled by monitor micro-controller 18 to spread monitor PN sequence 65 at chipping frequency, outputting a single bit value at the chipping rate of shifted version of monitor PN sequence 65 to second monitor modulator 21. Each time first shift register 13 or second shift register 15 has shifted monitor PN sequence 65 a number of times equal to the length of monitor PN sequence 65, first shift register 13 or second shift register 15 are re-loaded from the respective “source” registers (not shown in Figure).

**[0017]** Following, monitor direct sequence spread spectrum signal (MDSSS) 52, i.e., monitor signal, is electrically connected to a monitor power amplifier 24 to boost monitor signal strength. Monitor power amplifier 24 sends monitor signal 52 through monitor duplexer 26, such as a Mini-circuits RCM-12-4-75 or a functional equivalent, directing monitor signal 52 to first monitor antenna 32, such as a circuit board patch antenna, for transmission to tracked unit 12. Preferably, monitor reference oscillator 16, monitor modulator 20, and monitor power amplifier 24 functional blocks would be contained in one electronic circuit such as a Phillips SA900 or a functional equivalent.

**[0018]** Figure 3 is a schematic of a tracked unit. Tracked unit 12 is a transponder. A tracked antenna 50 on tracked unit 12 receives monitor signal 52. Preferably, tracked antenna 50 is a circuit board patch antenna. Tracked duplexer 46, such as Mini-circuits RCM 12-4-75 or alternatively a functional equivalent, electrically connects MDSSS 52 to a tracked low noise amplifier (LNA) 48, such as Agilent ABA-51563 or a functional equivalent, for signal amplification. Afterwards, monitor PN sequence transmitted within monitor signal 52 is mixed with tracked PN reference sequence within a tracked phase detector, in which in this embodiment is a tracked mixer 42, such as Minicircuits ADE-2 or a functional equivalent. Tracked mixer 42 generates a tracked PN error sequence 77, which tracked PN error sequence 77 is transmitted to a tracked micro-controller 39, such as Microchip PIC16F873, Altera NEOS inside EP1C3, or a functional equivalent circuit, which controls a tracked programmable logic array 36, such as Altera EP1C3 or a functionally equivalent circuit.

**[0019]** Tracked programmable logic array 36 shifts tracked PN sequence 61 and outputs a shifted version of tracked PN sequence 61 to a tracked modulator 40. Tracked modulator 40 outputs modulated tracked PN sequence 61, which shifted version of tracked PN sequence 61 is input to tracked mixer 42, wherein tracked PN error sequence 77 is again regenerated. Afterwards, the above steps are repeated until tracked PN sequence 61 locks with monitor PN sequence 65. As such, the above steps of regenerating monitor PN sequence within tracked unit 12 to adjust for phase errors generated within tracked unit 12, avoids phase inaccuracies introduced by conventional transponders that don't regenerate monitor PN sequence 65. After locking between tracked PN sequence 61 and monitor PN sequence 65 occurs, tracked modulator 40 outputs a tracked direct sequence spread spectrum signal (TDSSS), i.e., tracked signal 56, outputting tracked signal 56 though tracked amplifier 44. Tracked amplifier 44

electrically connects to tracked antenna 50 through tracked duplexer 46 for transmitting TDSSS signal 56 to monitor unit 10.

**[0020]** TDSSS signal 56 embodies three frequencies, i.e., a carrier frequency 72 (Figure 6), a chipping frequency 61 (Figure 5), and a repetition rate 55 (Figure 4) (i.e., the rate of repetition of monitor PN sequence). These three frequencies have an associated wavelength for one complete cycle. Using monitor phase detector 22 measurements, a user compares phase of tracked signal 56 to monitor signal 52 for various frequency components. These phase differences between tracked signal 56 to monitor signal 52 is used to determine ranging distance between monitor unit 10 and tracking unit 12. The ranging distance results from that portion of the wavelength that corresponds to a proportion of a phase difference as compared with a full cycle, i.e., 360 degrees. Coarse distance calculation is done with the largest wavelength frequency. Afterwards, coarse distance calculation is used in conjunction with a smaller wavelength frequency component to determine with increased accuracy the ranging distance for the portion of the distance that is in excess of an integer number of wavelengths. Prior art would have counted within a digital counter the number of frequency intervals that are repeated while this present invention would compare phase shift between different frequencies of DSSS signal 56 and MSSS signal 52, and use these phase differences of each frequency component for measuring ranging distance between tracked unit 12 and monitor unit 10.

**[0021]** Referring to Figure 2, a first monitor antenna 32 and a second monitor antenna 34 receives tracked signal 56. In this embodiment, first monitor antenna 32 and second monitor antenna 34 are cross-polarized. Antenna switch 30 selects first monitor antenna 32. Tracked signal 56 received by first monitor antenna 32 electrically connects to monitor duplexer 26, which directs tracked signal 56 to monitor low noise amplifier 28 for boosting signal level. Afterwards, tracked signal 56 is frequency mixed with monitor signal 52 to generate a monitor PN error signal 83. Monitor PN error signal 83 is electrically connected to monitor micro-controller 18. Monitor micro-controller 18 generates a monitor PN shift control 85 which is electrically connected to monitor programmable logic array 14. Monitor programmable logic array 14 shifts monitor PN sequence 65 and applies a shifted version of monitor PN sequence 65 (Figure 5) to second modulator 21 that connects to monitor mixer 22. In this embodiment,

monitor PN sequence 65 has a repetition rate 57, i.e., monitor first frequency component, of 250 kHz.

[0022] Figure 4 illustrates first phase comparison technique of monitor unit 10 interferometer of present invention. In particular, detection of TDSSS 56 involves shifting monitor repetition rate 57 until it locks with tracked first frequency component 55 of TDSSS signal 56. As explained above in paragraph [0016] monitor programmable logic array 14 implements a first shift register 13 (shown in Figure 2) and a second shift register 15 (Shown in Figure 2) and a digitally controlled tap (not shown in Figure) along each shift register to calculate a first phase difference 59 between tracked first frequency component 55 relative to monitor repetition rate 57, i.e., monitor first frequency component. Afterwards, first phase comparison is repeated until tracked first frequency component 55 locks with monitor repetition rate 57. In this embodiment, within monitor programmable logic array 14, a phase comparison is made between a tracked first frequency component 55, i.e., a 250 KHz and a monitor repetition rate 57 of 250 KHz using a first shift register 13 and second shift register 15, as shown in Figure 2, to calculate first phase difference 59.

[0023] First phase difference 59 determines a coarse measure of ranging distance between tracked unit 12 and monitor unit 10. In this embodiment, a first frequency comprising a 250 kHz reference frequency has roughly a wavelength in free space of 4,000 feet. As such, first phase difference 59 is appropriate for creating a ranging distance error between monitor unit 10 and tracked unit 12 of less than 4,000 feet. In practice, monitor mixer 22 generates a PN phase error, typically around 5 degrees, which gives a first phase difference measurement ranging distance error of over 55 feet. Thus, we still need more accuracy of ranging distance between tracked unit 12 and monitor unit 10.

[0024] Figure 5 illustrates a second phase comparison of a monitor unit 10 of the present invention. As such, to achieve more accuracy, we need to use a second tracked frequency 61 of TDSSS signal 56 having a medium wavelength. As such monitor programmable logic array 14, a second phase comparison is made between a monitor PN sequence 65 (a second monitor frequency component) having a chipping frequency of 12 MHz compared to TDSSS 56 including a second tracked frequency component 61, which is in this embodiment a 12 MHz signal, to generate a second phase difference 63. Second phase difference 63 is used in conjunction with first phase difference 59 to increase accuracy of determining distance between

tracked unit 12 and monitor unit 10. For example, second tracked frequency component 61, which second tracked frequency component 61 has a wavelength of about 69 feet, which determines the number of cycles of 69 feet involved in determining ranging distance of tracked unit 12. Prior art would have counted pulses by determining the number of constructive and destructive phase differences between a reference signal and received signal while the present invention uses more than one phase detection. More specifically, this invention uses more than one phase detection to measure with increasing accuracy, i.e., with increasingly more accurate discrete steps, to generate ranging distance between tracked unit 12 and monitor unit 10.

[0025] Figure 6 illustrates a third phase comparison of a monitor unit 10 of the present invention. To achieve even more accuracy, this step involves using a third tracked frequency component 72, i.e. 900 MHz band, of the TDSSS signal 56, where monitor programmable logic array 14 performs a third phase comparison between monitor reference frequency 70, i.e., monitor third frequency component, which in this embodiment is carrier frequency, and a tracked third frequency component 72, outputting a third phase difference 74, wherein carrier frequency has a wavelength on the order of 1 foot. This third phase comparison provides very fine step resolution in conjunction with coarse phase error 59 to increase calculation accuracy of tracked unit ranging distance from monitor unit.

[0026] Referring to Figure 2, a measurement of a received signal strength indication (RSSI) from first monitor antenna 32 is made based on amplitude difference between monitor signal 52 and TDSSS signal 56. Following, antenna switch 30 connects second monitor antenna 34, measures shift level, and calculates amplitude difference between monitor signal 52 and TDSSS signal 56. A difference is calculated between first monitor antenna 32 and second monitor antenna 34 received power levels to determine if tracked unit 12 getting closer to monitor unit 10. From this difference in first monitor antenna 32 measured power and second monitor antenna 34 measured power, a relative angle of tracked unit 12 is determined in relation to monitor unit 10. As such, the ratio of power from first monitor antenna 32 to second monitor antenna 34, minus first monitor antenna loss and second monitor antenna loss, equates to the cotangent of an angle 51 relative to axis of first monitor antenna 32.

[0027] Figure 7 illustrates a front view of a housing for a tracked unit for the present invention. A tracked unit 12 (not shown) is disposed on an integrated circuit that is placed within tracked unit housing 94 where first strap 90 and second strap 92 are attached to a person

to be tracked, such as a child. Further, first button 96 activates or deactivates the tracked unit (not shown). In this embodiment, the tracked unit housing 94 includes a working watch where second button 98 sets the watch time and third button 102.

[0028] Figure 8 illustrates a front view and a back view of a housing for a monitor unit for the present invention. A monitor unit 10 (not shown in Figure) is disposed on an integrated circuit (not shown in Figure) placed within monitor unit housing 101, where monitor microcontroller 18 is electrically connected to a monitor compass 104, such as a Liquid Crystal Display (LCD), which monitor compass hand 105 displays location of a tracked unit 12 (not shown in Figure). A user may adjust a first range adjustment dial 107 and a second range adjustment dial 108, allowing a user to select a zone, whereby monitor unit 10 tracks a tracked unit 12 (not shown in Figure).

[0029] Figure 9 displays operating field of activity of the system. In this embodiment, there are four zones of coverage. Each zone shows a user a relative distance between a monitor unit and a tracked unit. A first zone 115, i.e., safe zone, means a tracked unit 10 is within close range of a monitor unit 10. In this Figure, object 122 including a tracked unit 12 (not shown) is within safe zone 115 and first light 110 (shown in Figure 8) is lit displaying that object 122 is within safe zone 115. In the alternative, if an object including a tracked unit (not shown in Figure) was within a second zone 117, which is a first ring of coverage away from safe zone 115, a second light 112 (shown in Figure 8) would be lit. In yet another alternative, if an object including a tracked unit 12 (not shown in Figure) is within a third zone 120, a third light 118 (shown in Figure 8) would be lit. In yet a further alternative, fourth zone 125 is the area outside of third zone of coverage 120. As such, each ring of coverage, i.e., second zone 117 and third zone 120 are concentric rings of coverage to alert a user of a relative change in ranging distance of said monitor unit 10 (Figure 1) to said tracked unit 12 (Figure 1).

[0030] Information herein shown and described in detail is fully capable of attaining the above-described object of the invention and the present embodiment of the invention, and is, thus, representative of the subject matter which is broadly contemplated by the present invention. The scope of the present invention fully encompasses other embodiments which may become obvious to those skilled in the art, and is to be limited, accordingly, by nothing other than the appended claims, wherein reference to an element in the singular is not intended to mean “one and only one” unless explicitly so stated, but rather “one or more.” All

structural and functional equivalents to the elements of the above-described embodiment and additional embodiments that are known to those of ordinary skill in the art are hereby expressly incorporated by reference and are intended to be encompassed by the present claims.

[0031] Moreover, no requirement exists for a device or method to address each and every problem sought to be resolved by the present invention, for such to be encompassed by the present claims. Furthermore, no element, component, or method step in the present disclosure is intended to be dedicated to the public regardless of whether the element, component, or method step is explicitly recited in the claims. However, one skilled in the art should recognize that various changes and modifications in form and material details may be made without departing from the spirit and scope of the inventiveness as set forth in the appended claims. No claim herein is to be construed under the provisions of 35 U.S.C. § 112, sixth paragraph, unless the element is expressly recited using the phrase “means for.”

#### INDUSTRIAL APPLICABILITY

[0032] This invention applies industrially to an electronic tracking and ranging system. More specifically, the present invention applies industrially to an object tracking and ranging system that utilizes phase differences between monitor reference signal and monitor received signal from multiple frequency bands to increase accuracy of locating objects. The present invention reduces positioning inaccuracy due to surrounding area.